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Bib Data Sheet

SERIAL NUMBER 10/774,818	FILING DATE 02/09/2004 RULE	CLASS 365	GROUP ART UNIT 2827	ATTORNEY DOCKET NO. 023-0025
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## APPLICANTS

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## \*\* CONTINUING DATA \*\*\*\*\*

This application is a CON of 10/253,354 09/23/2002 PAT 6,780,711  
 which is a CON of 09/939,431 08/24/2001 PAT 6,483,736  
 which is a CON of 09/714,440 11/15/2000 PAT 6,351,406  
 which is a CON of 09/469,658 12/22/1999 PAT 6,185,122  
 which is a DIV of 09/192,883 11/16/1998 PAT 6,034,882

*yes, TL*

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

*None, TL*

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 05/08/2004

Foreign Priority claimed	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance	CA	DRAWING 24	CLAIMS 44	CLAIMS 3
Verified and Acknowledged	Examiner's Signature <i>TL</i> Initials				

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## TITLE

Integrated circuit incorporating three-dimensional memory array with dual opposing decoder arrangement

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